

**IN THE SPECIFICATION:**

**Please replace paragraph 2 at page 5, with the following rewritten paragraph:**

Subsequently, the FAT in the embedded memory is referred to, the next cluster address CL2 is read from the space at FAT address 1 corresponding to cluster address CL1, and the file A-2 is read from the cluster at cluster address CL2. Then, the FAT in the embedded memory is referred to, the next cluster address CL3 is read from the space at FAT address 2 corresponding to cluster address CL2, and the file A-3 is read from the cluster at cluster address ~~CL12~~ CL3.

**Please replace paragraphs 5 and 6 at page 14, with the following rewritten paragraphs:**

Fig. 6 ~~is a diagram for explaining a FAT block~~ is a block diagram showing an example of the structure of an image-recording apparatus according to an embodiment of the present invention.

Fig. 7 ~~is a block diagram showing an example of the structure of an image-recording apparatus according to an embodiment of the present invention~~ is a diagram for explaining a FAT block.

**Please replace paragraph 2 at page 22, with the following rewritten paragraph:**

The process of image recording will now be described with reference to the flowchart in Fig. 9. First, a user performs an operation to start image recording. In response, the CPU 1 instructs each section of the image-recording apparatus to start image recording. Thus, the process of image recording starts when each section starts its operation (for example, when the video camera 7 outputs video signals, the video encoder 8 outputs encoded video data, the mixer ~~+~~ 11 outputs program streams, and data begins to accumulate in the data buffer 13).